

503.35686CX3

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: Tetsuya SHIMOMURA et al.
Application No.: Not yet assigned
(Continuation of 09/991,705, filed November 26, 2001)
Filing Date: Even date herewith
Title: DATA PROCESSOR HAVING UNIFIED MEMORY
ARCHITECTURE USING REGISTER TO OPTIMIZE MEMORY
ACCESS (AS AMENDED)
Art Unit: 2671
Examiner: Ulka J. Chauhan

PRELIMINARY AMENDMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

September 25, 2003

Sir:

Prior to examination, please enter thereto the following Preliminary
Amendment.

Amendments to the Specification begin on page 2 of this paper.

Remarks/Arguments begin on page 47 of this paper.